

FIG. 1

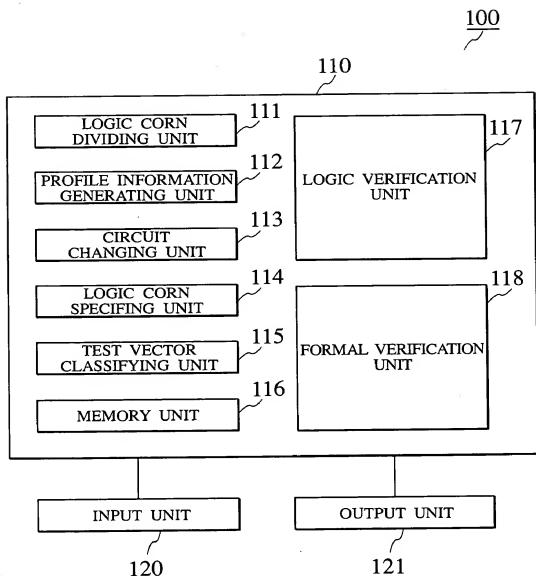
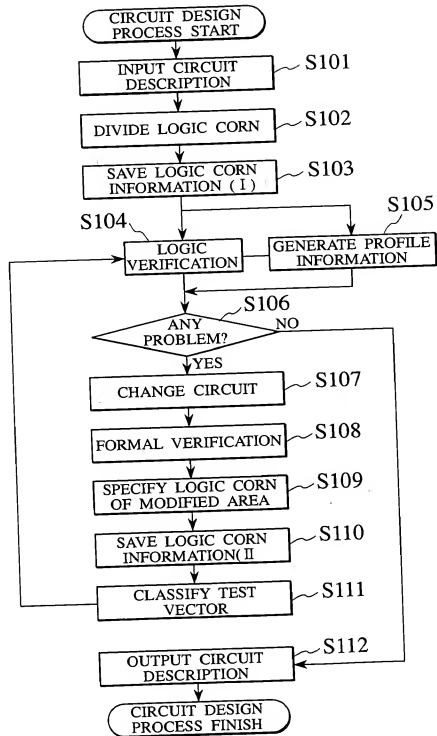


FIG. 2



006290.84790960

FIG. 3

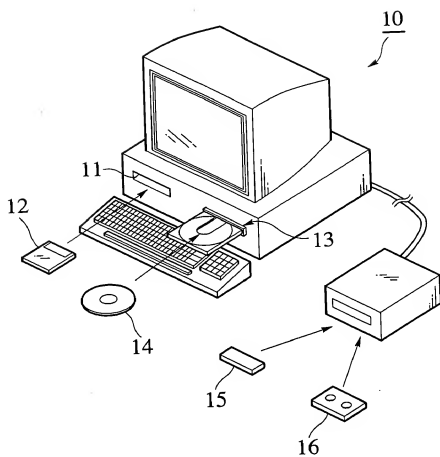


FIG. 4A

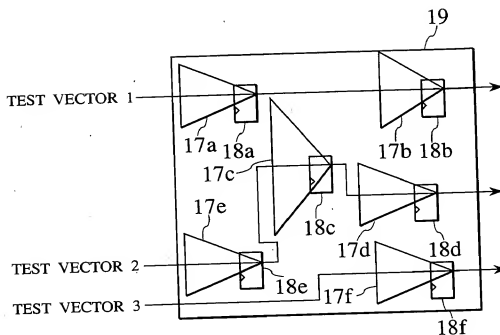
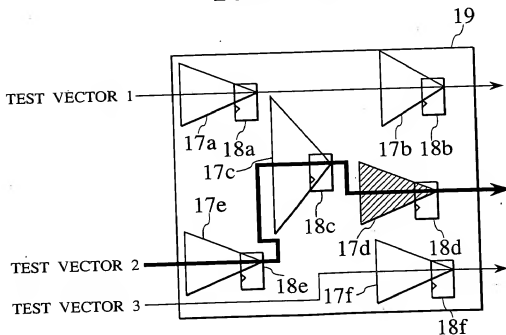


FIG. 4B



17a,b,c,d,e,f: LOGIC CORN

18a,b,c,d,e,f: REGISTER

19: INTEGRATED CIRCUIT

FIG. 5A

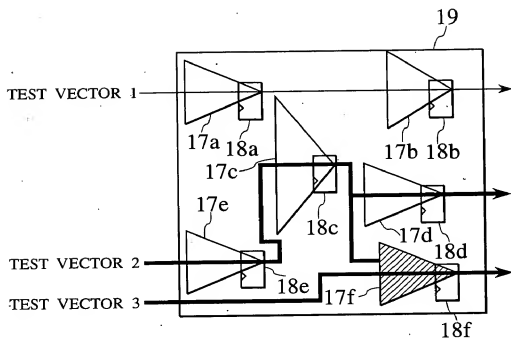
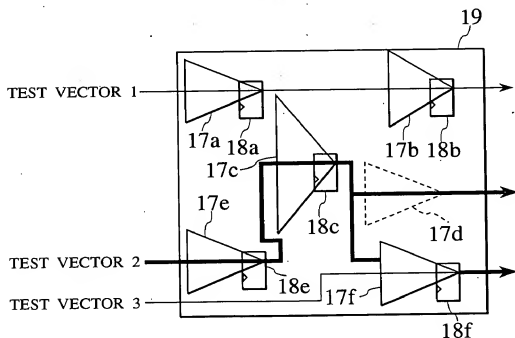


FIG. 5B



17a,b,c,d,e,f: LOGIC CORN

18a,b,c,d,e,f: REGISTER

19: INTEGRATED CIRCUIT